Renesas Electronics Europe (REE) is a dynamic, multi-cultural tech company where employees can learn, mentor and thrive. REE UK brings together the strong financial foundation of a multi-billion-dollar global operation and the flexibility and velocity of a smaller organization. We are developing technologies for the latest advances in mobile computing, secured connected devices, autonomous driving, smart homes, factories and more. Our solutions are at the heart of products developed by the major innovators around the world. Join us and be part of what’s next in electronics. For more information about Renesas, please visit [Jobs @ Renesas](https://jobs.renesas.com).

**Intern (Harlow)**

The Systems Team and the Design Team at Renesas in Harlow are looking for a talented engineering student to join our team this Summer as a Systems/Design Intern in our Harlow office. Our team uses state of the art methodology to ensure successful execution of mixed signal IC developments, including architecture, design, and verification. Previous internships have included leading the evaluation of a cutting-edge switching regulator modelling tool, as well as using GPT AI models to generate code that is part of our latest automated flows. The successful candidate will work directly with a technical mentor from the Systems/Design Team who will collaboratively to both define a project and create a training program with the intern. The goal will be to align the project with the interests of the intern, while making it relevant to the broader work of the team as we evolve our methodology. The training will be both technical and non-technical, and tailored to the intern’s interests and needs.

**Primary Responsibilities:**

* Work with mentor to define project scope
* Agree on milestones and targets
* Create training plan for both technical and non-technical development
* Present updates and final work to the team and beyond as applicable

**Relevant Qualifications/Training:**

* Good understanding of electronics fundamentals
* Coursework/experience in Analog/Mixed Signal IC Design
* Experience with coding, especially any HDL (Verilog, SystemVerilog)
* Experience of the Cadence Virtuoso IC Design Environment is desirable but not essential.